



## Description

### JMT N-channel Enhancement Mode Power MOSFET

#### Features

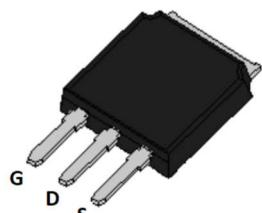
- 40V,60A  
 $R_{DS(ON)} < 7\text{m}\Omega$  @  $V_{GS} = 10\text{V}$   
 $R_{DS(ON)} < 12\text{m}\Omega$  @  $V_{GS} = 4.5\text{V}$
- Lead free and Green Device Available
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

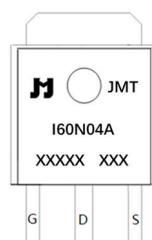
- Load Switch
- PWM Application
- Power management



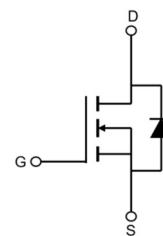
100% UIS TESTED!  
100%  $\Delta V_{ds}$  TESTED!



TO-251-4R top view



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
JMTI60N04A	JMTI60N04A	TUBE	TO-251-4R	80	4000	32000

## Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		40	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ\text{C}$	60	A
		$T_c = 100^\circ\text{C}$	39	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		240	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>		81	mJ
$P_D$	Power Dissipation	$T_c = 25^\circ\text{C}$	47	W
$R_{eJC}$	Thermal Resistance, Junction to Case		3.2	$^\circ\text{C}/\text{W}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

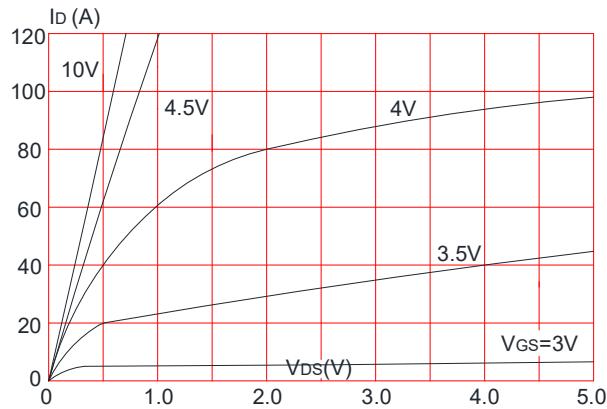
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	40	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.1	1.7	2.4	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=30\text{A}$	-	5.5	7	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=20\text{A}$	-	9	12	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	2400	-	pF
$C_{oss}$	Output Capacitance		-	192	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	165	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=20\text{V}$ , $I_D=30\text{A}$ , $V_{GS}=10\text{V}$	-	37	-	nC
$Q_{gs}$	Gate-Source Charge		-	6	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	7	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=20\text{V}$ , $I_D=30\text{A}$ , $R_L=1\Omega$ , $R_{\text{GEN}}=3\Omega$ , $V_{GS}=10\text{V}$	-	12	-	ns
$t_r$	Turn-on Rise Time		-	12	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	38	-	ns
$t_f$	Turn-off Fall Time		-	9	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	60	-	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	240	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=30\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C}$ , $I_F=20\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	22	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	11	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

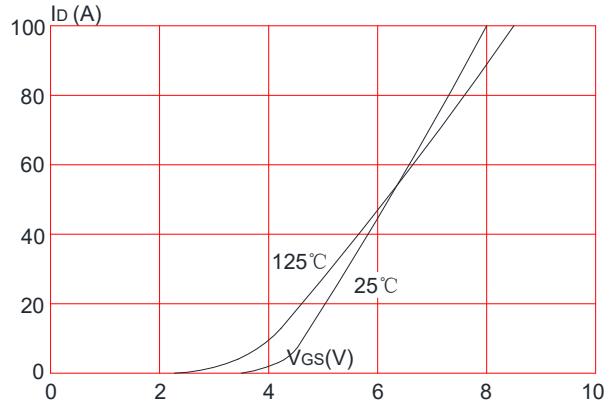
2. EAS condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD}=20\text{V}$ ,  $V_G=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=18\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

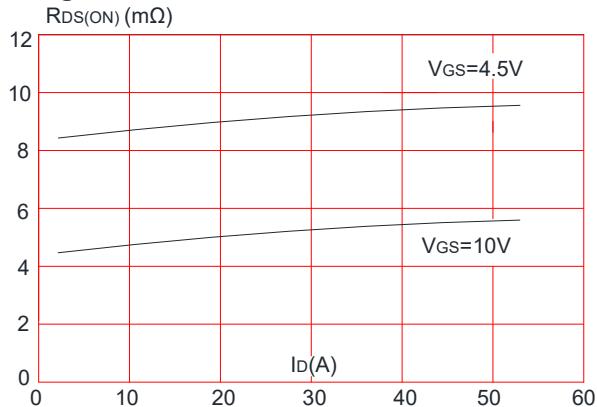
**Figure 1:** Output Characteristics



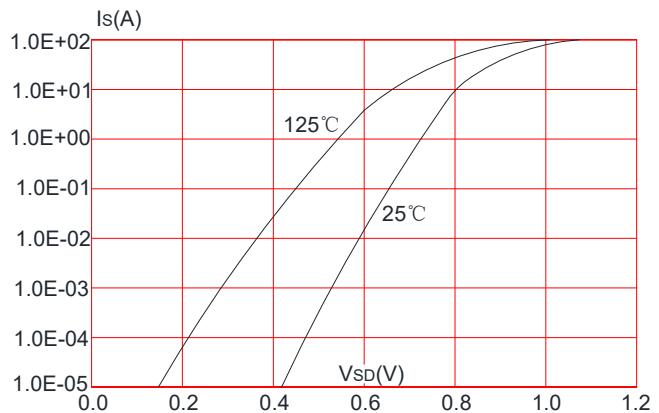
**Figure 2:** Typical Transfer Characteristics



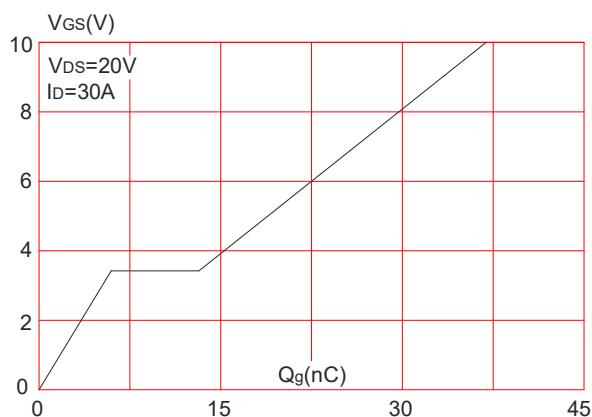
**Figure 3:** On-resistance vs. Drain Current



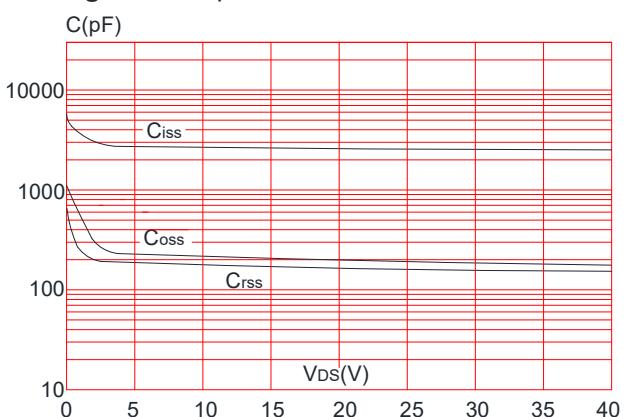
**Figure 4:** Body Diode Characteristics



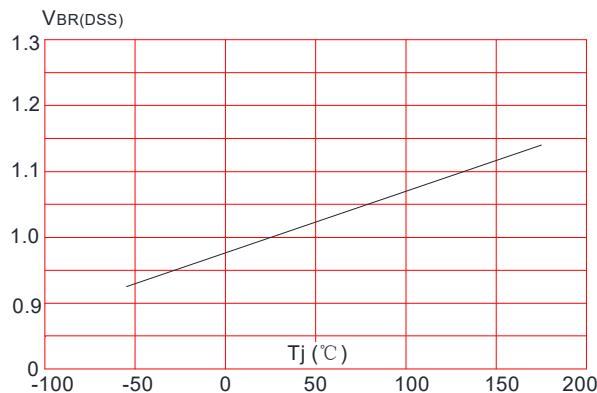
**Figure 5:** Gate Charge Characteristics



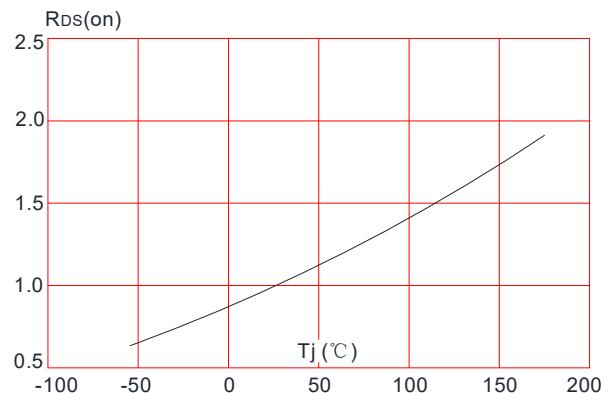
**Figure 6:** Capacitance Characteristics



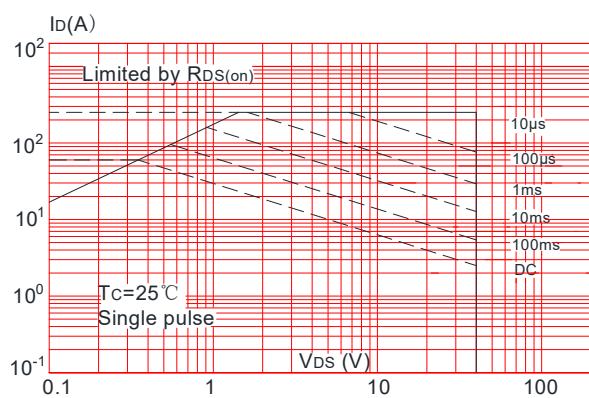
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



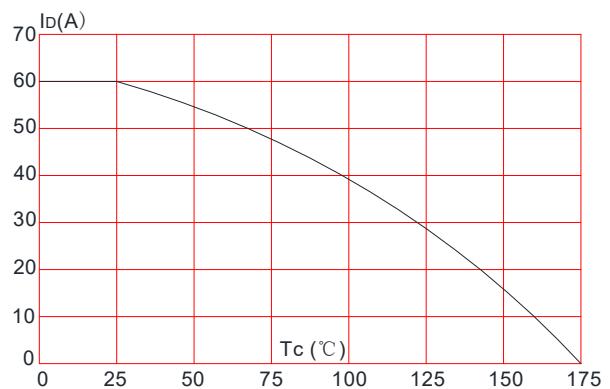
**Figure 8:** Normalized on Resistance vs. Junction Temperature



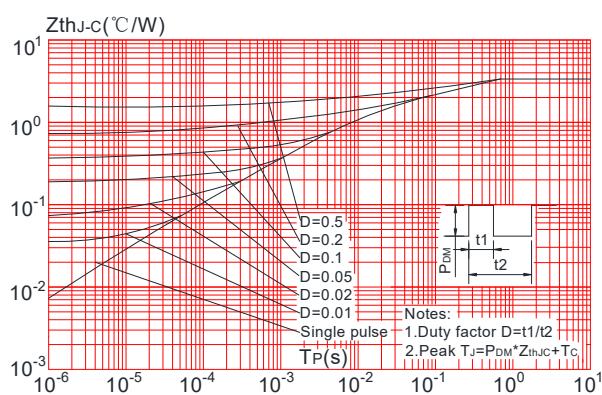
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



## Test Circuit



Figure1:Gate Charge Test Circuit & Waveform

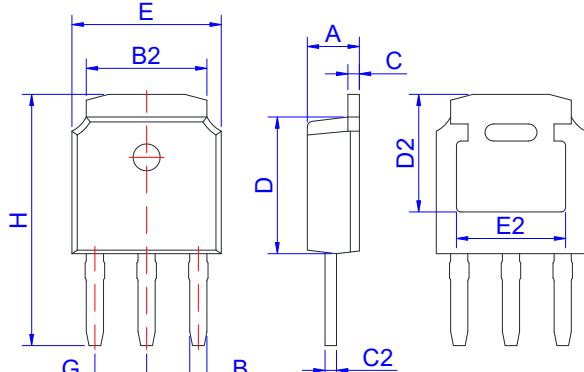


Figure 2: Resistive Switching Test Circuit & Waveforms



Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Package Mechanical Data-TO-252-4R



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10	2.30	2.50	0.083	0.091	0.098
B	0.66	0.76	0.86	0.026	0.030	0.034
B2	5.15	5.33	5.48	0.203	0.210	0.216
C	0.44	0.51	0.58	0.017	0.020	0.023
C2	0.44	0.51	0.58	0.017	0.020	0.023
D	5.90	6.10	6.30	0.232	0.240	0.248
D2	5.30 REF			0.209 REF		
E	6.40	6.60	6.80	0.252	0.260	0.268
E2	4.83 REF			0.190 REF		
G	2.19	2.29	2.39	0.086	0.090	0.094
H	10.60	11.20	11.80	0.417	0.441	0.465

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